

Notice of References Cited	Application/Control No. 10/505,260	Applicant(s)/Patent Under Reexamination DALTON, DAMIAN	
	Examiner David Silver	Art Unit 2128	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
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FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Damian Dalton, "Avoiding Conventional Overheads in Parallel Logic Simulation: A New Architecture", Publisher: Springer Berlin / Heidelberg ISSN: 0302-9743 (Print) 1611-3349 (Online), DOI: 10.1007/b86069 Copyright: 1999
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